



SPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814

Examiner: Theresa T. Doan

In Re PATENT APPLICATION of:

Applicant(s): Hirotaka KOMATSUBARA

Serial No.: 10/812,959

Filing Date: March 31, 2004

For: SEMICONDUCTOR DEVICE WITH
REDUCED INTERCONNECT
CAPACITANCE

Atty. Docket: MAE 309

**RESPONSE TO
RESTRICTION
REQUIREMENT**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Restriction Requirement dated June 16, 2005, Applicant elects the invention of Group I, i.e., the subject matter of claims 1-7 (Figure 1 and Figures 3E-3H), for further examination. The election is made without traverse.

Examination of the application on the merits is respectfully requested.

Respectfully submitted,

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July 15, 2005
Date

RHB:vm

FEE ENCLOSED: \$~~0~~
Please charge any further
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